

SPECIFICATION		
SERIES NUMBER	QXO-SERIES	
PACKAGE DESIGNATOR	DIP-14 and DIP-8	
NOMINAL FREQUENCY	0.455 MHz to 175 MHz	
STORAGE TEMPERATURE RANGE	-50°C to +125°C	
FREQUENCY STABILITY DESIGNATOR Refer Note 1.	'A' : ±25ppm 0°C to 70°C 'B' : ±50ppm 0°C to 70°C 'C' : ±100ppm 0°C to 70°C 'D' : ±10ppm 0°C to 70°C 'X' : Customer Specified	'E' : ±25ppm -40°C to +85°C 'F' : ±50ppm -40°C to +85°C 'G' : ±100ppm -40°C to +85°C 'H' : ±20ppm -40°C to +85°C
OUTPUT LOAD	50pF or 10 TTL Gates	
INPUT CURRENT	1.0 MHz to 24.0 MHz 25mA Maximum 24.0 MHz to 50.0 MHz 30mA Maximum 50.0 MHz to 70.0 MHz 40mA Maximum 70.0 MHz to 175.0 MHz 50mA Maximum	
LOGIC '1' LEVEL	90% Vdd Minimum	
LOGIC '0' LEVEL	10% Vdd Minimum	
SYMMETRY (DUTY CYCLE) (Refer Note 3.)	'A' : 50% ±10% Maximum at 0.5 Vdd (Standard and default) 'B' : 50% ±5% Maximum at 0.5 Vdd (Option)	
RISE AND FALL TIME	4 nSec. Maximum 20% to 80% Vdd	
INPUT VOLTAGE	3.3 Volt D.C. 10% 5 Volt D.C. 10% operating typical, 7 Volt D.C. Maximum	
OPTION ON PIN 1 Refer Note 2.	'0' Disable	
DISABLE PHASE DELAY	100 nSec. Maximum	

Note 1. Inclusive of 25°C tolerance, operating temperature range. ±10% input voltage variation, load change, aging, shock and vibration.

Note 2. When '0' (≤ 0.8 Volt) is applied to the pin number 1 the output becomes high impedance. The output becomes active when '0' level is removed.

Note 3. If symmetry not requested then standard default will apply.

PART NUMBER FORMAT

(Package designator) - (Stability designator) (Supply voltage and Pin Number 1 option) (Symmetry) - (Frequency)

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'A' : 5 Volt, Pin No. 1 Not connected
'B' : 5 Volt, Pin No. 1 Enable/Disable
'C' : 3.3 Volt, Pin No. 1 Not connected
'D' : 3.3 Volt, Pin No. 1 Enable/Disable

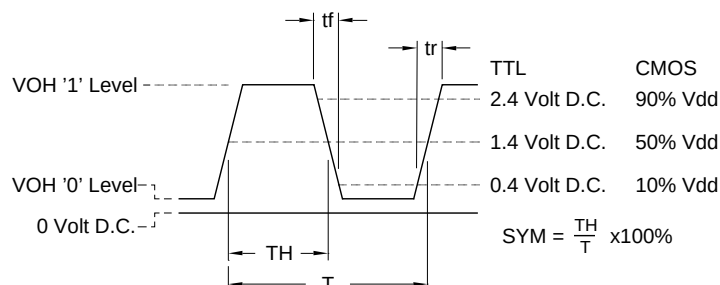
EXAMPLE:- 14CAA-20.000

Represents: DIP-14 Oscillator, 20.000 MHz, ± 100 ppm, 5 Volt, with no connection on Pin number 1, Symmetry 40 / 60%

EXAMPLE:- 8BDB-16.000

Represents: DIP-8 Oscillator, 16.000 MHz, ± 50 ppm, 3.3 Volt, enable/disable on Pin number 1, Symmetry 45 / 55%

OUTPUT WAVEFORM:-



TEST CIRCUIT DIAGRAM:-

